

L Number	Hits	Search Text	DB	Time stamp
1	1117	(plural\$3 or second\$3) near3 alu	USPAT; US-PGPUB	2003/12/10 13:46
2	994	register\$1 and ((plural\$3 or second\$3) near3 alu)	USPAT; US-PGPUB	2003/12/10 13:46
3	126382	input/output	USPAT; US-PGPUB	2003/12/10 13:46
4	402	(register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output	USPAT; US-PGPUB	2003/12/10 13:47
5	390322	microprocessor or chip	USPAT; US-PGPUB	2003/12/10 13:47
6	338	((register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output) and (microprocessor or chip)	USPAT; US-PGPUB	2003/12/10 13:47
7	269	switch\$3 and (((register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output) and (microprocessor or chip))	USPAT; US-PGPUB	2003/12/10 13:47
8	698350	program\$3 or software or instruction\$1	USPAT; US-PGPUB	2003/12/10 13:48
9	268	(switch\$3 and (((register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output) and (microprocessor or chip))) and (program\$3 or software or instruction\$1)	USPAT; US-PGPUB	2003/12/10 13:48
10	80690	(matrix or grid or crossbar or array) near3 (interconnect\$3 or connect\$3 or switch\$3)	USPAT; US-PGPUB	2003/12/10 13:50
11	95	((switch\$3 and (((register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output) and (microprocessor or chip))) and (program\$3 or software or instruction\$1)) and ( (matrix or grid or crossbar or array) near3 (interconnect\$3 or connect\$3 or switch\$3))	USPAT; US-PGPUB	2003/12/10 14:49
12	235	reconfigur\$5 near3 microprocessor	USPAT; US-PGPUB	2003/12/10 14:50
13	0	((register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output) and (reconfigur\$5 near3 microprocessor)	USPAT; US-PGPUB	2003/12/10 14:50
14	0	((plural\$3 or second\$3) near3 alu) and (((register\$1 and ((plural\$3 or second\$3) near3 alu)) and input/output) and (reconfigur\$5 near3 microprocessor))	USPAT; US-PGPUB	2003/12/10 14:51
15	82	slic\$3 near3 alu	USPAT; US-PGPUB	2003/12/10 15:13
16	1122	(multiple or second\$3 or plural\$3) near3 (arithmetic adj logic)	USPAT; US-PGPUB	2003/12/10 15:15
17	166	reconfigur\$5 and ((multiple or second\$3 or plural\$3) near3 (arithmetic adj logic))	USPAT; US-PGPUB	2003/12/10 15:15
18	164	register\$1 and (reconfigur\$5 and ((multiple or second\$3 or plural\$3) near3 (arithmetic adj logic)))	USPAT; US-PGPUB	2003/12/10 15:16
19	854793	switch\$3 or (interconnect\$3 near3( grid or array or matrix))	USPAT; US-PGPUB	2003/12/10 15:17
20	133	(register\$1 and (reconfigur\$5 and ((multiple or second\$3 or plural\$3) near3 (arithmetic adj logic)))) and (switch\$3 or (interconnect\$3 near3( grid or array or matrix)))	USPAT; US-PGPUB	2003/12/10 15:28
21	1173	select\$3 near3 alu	USPAT; US-PGPUB	2003/12/10 15:28
22	27	connect\$3 near3 (select\$3 near3 alu)	USPAT; US-PGPUB	2003/12/10 15:30
23	18	multiprocessor with alu	USPAT; US-PGPUB	2003/12/10 15:35
24	306	712/11.ccls.	USPAT; US-PGPUB	2003/12/10 15:36
25	80	alu and 712/11.ccls.	USPAT; US-PGPUB	2003/12/10 16:20
26	152	alu with pe	USPAT; US-PGPUB	2003/12/10 16:22
27	665028	array or matrix or grid	USPAT; US-PGPUB	2003/12/10 16:22
28	110	(alu with pe) and (array or matrix or grid)	USPAT; US-PGPUB	2003/12/10 16:35

29	54	column near3 alu	USPAT; US-PGPUB	2003/12/10 16:35
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